

What is claimed is:

1. An apparatus for driving a flat display panel comprising a scan driving unit for controlling an upper voltage value and a lower voltage value which are applied to an IC (Integrated Circuit) for driving a scan electrode of a flat display panel.

2. The apparatus of claim 1, wherein the scan driving unit further comprises an amplifying unit for amplifying the upper voltage value to a predetermined level.

3. The apparatus of claim 2, wherein the amplifying unit comprises an OP-AMP (Operational Amplifier).

4. The apparatus of claim 1, wherein the scan driving unit further comprises an amplifying unit for converting the upper voltage value to a current and amplifying the converted current to a predetermined level.

5. The apparatus of claim 4, wherein the amplifying unit comprises an OP-AMP and a TR (transistor) connected to an output terminal of the OP-AMP.

6. The apparatus of claim 1, wherein the scan driving unit comprises: an upper voltage generating unit for outputting an upper voltage value on the basis of an upper switching control signal; and

a lower voltage generating unit for outputting a lower voltage value on the

basis of a lower switching control signal.

7. The apparatus of claim 6, wherein the scan driving unit selectively outputs one of the outputted upper voltage value and the outputted lower voltage value, on the basis of a timing control signal.

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8. The apparatus of claim 7 comprises switching devices having a push-pull form turned on/off on the basis of the timing control signal.

9. The apparatus of claim 8, wherein the switching devices
10 comprises a FET (Field Effect Transistor).

10. The apparatus of claim 6, wherein the upper voltage generating unit comprises switching devices having a push-pull form turned on/off on the basis of the upper switching control signal.

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11. The apparatus of claim 10, wherein the switching devices comprise a FET (Field Effect Transistor).

12. The apparatus of claim 6, wherein the lower voltage generating
20 unit comprises switching devices having a push-pull form turned on/off on the basis of the upper switching control signal.

13. The apparatus of claim 12, wherein the switching devices comprise a FET (Field Effect Transistor).

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